

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

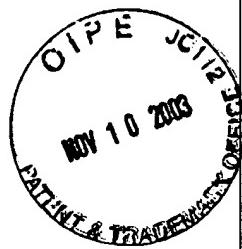
In re Patent Application of

Douglas Brisbin et al.

Application No.: 10/616,381

Filed: July 9, 2003

For: LDMOS TRANSISTOR STRUCTURE  
FOR IMPROVING HOT CARRIER  
RELIABILITY



Group Art Unit: 2811

Examiner: Unknown

**SUBMISSION OF FORMAL DRAWINGS  
IN RESPONSE TO NOTICE TO FILE  
MISSING PARTS OF NONPROVISIONAL  
APPLICATION MAILED  
OCTOBER 15, 2003**

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1490, Alexandria, VA 22313-1450 on Nov. 5, 2003.

STALLMAN & POLLOCK LLP

Dated: 11/5/2003 By: Georgia K. Smith  
Georgia K. Smith

Sir:

Pursuant to the Notice to File Missing Parts of Nonprovisional Application, attached for filing in the above-reference application are four (4) sheets of formal drawings to replace the original drawings filed on July 9, 2003.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: November 4, 2003

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